

WEST Search History

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DATE: Friday, June 09, 2006

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DB=PGPB,USPT,JPAB; PLUR=YES; OP=OR

| | | |
|------------------------------|---|-----|
| <input type="checkbox"/> L15 | L12 and (clock same cycle) | 48 |
| <input type="checkbox"/> L14 | L12 and (clock same cycle) and fpga | 2 |
| <input type="checkbox"/> L13 | emulator same clock same share same (dut or test\$ or device) | 43 |
| <input type="checkbox"/> L12 | emulator same clock same share | 51 |
| <input type="checkbox"/> L11 | L1 and (shar\$4 adj3 clock) | 7 |
| <input type="checkbox"/> L10 | L1 and (clock adj4 shar\$4) | 6 |
| <input type="checkbox"/> L9 | L4 and (clock adj4 shar\$4) | 0 |
| <input type="checkbox"/> L8 | L4 and (clock adj shar\$4) | 0 |
| <input type="checkbox"/> L7 | L6 and (interrupt or exception) | 8 |
| <input type="checkbox"/> L6 | L4 and (clock same shar\$4) | 22 |
| <input type="checkbox"/> L5 | L4 and share | 31 |
| <input type="checkbox"/> L4 | L3 and (clock adj cycle) | 45 |
| <input type="checkbox"/> L3 | L2 and fpga | 71 |
| <input type="checkbox"/> L2 | L1 and (clock same signal) | 214 |
| <input type="checkbox"/> L1 | clock\$ same emulat\$3 same (share or sharing or shared or distribut\$) | 235 |

END OF SEARCH HISTORY